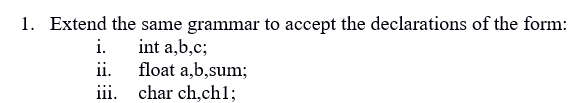
19CSE401 - Compiler Design

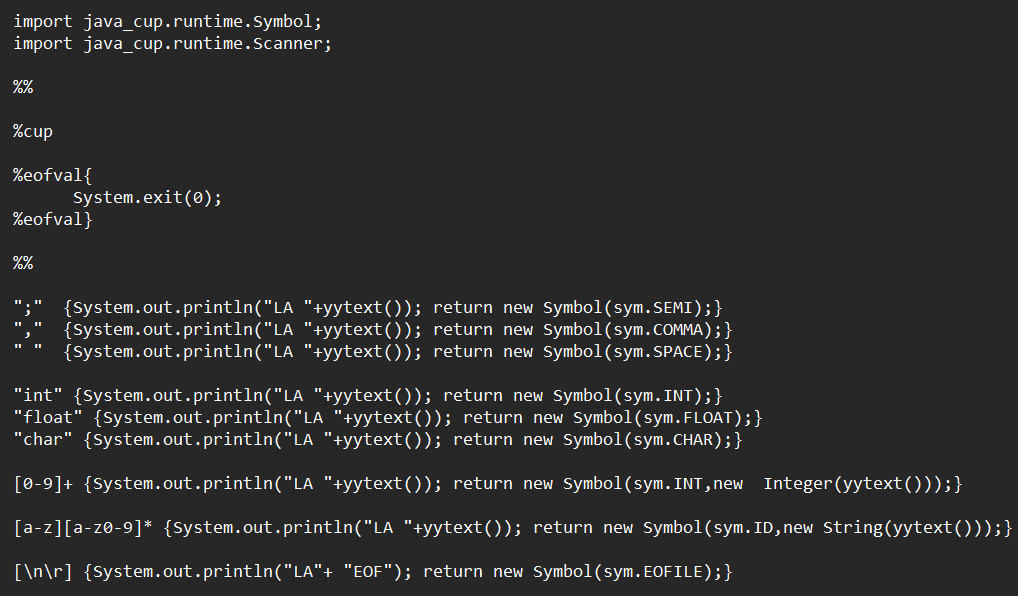
Lab Sheet 6

S Abhishek

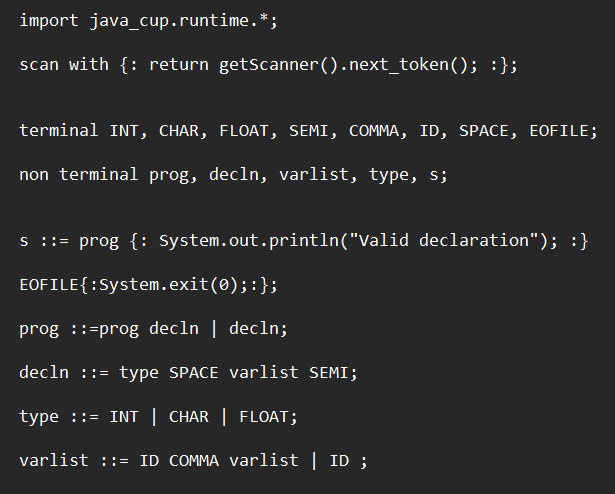
AM.EN.U4CSE19147



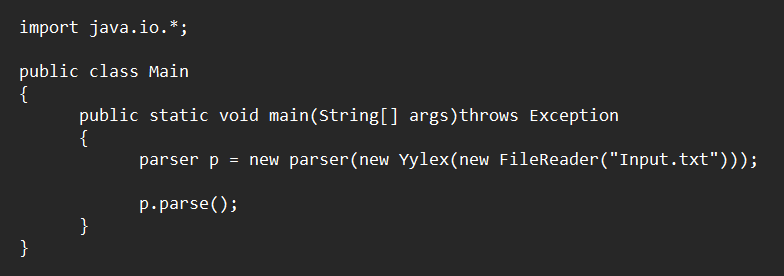
Jlex File



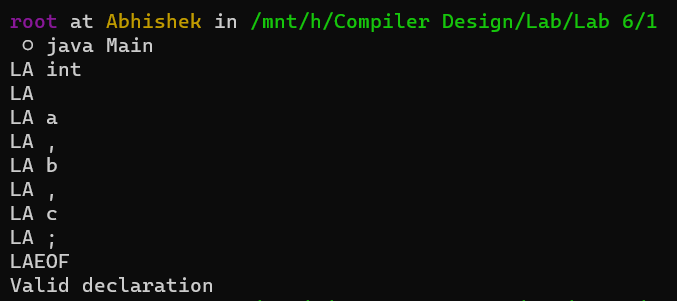
Cup File

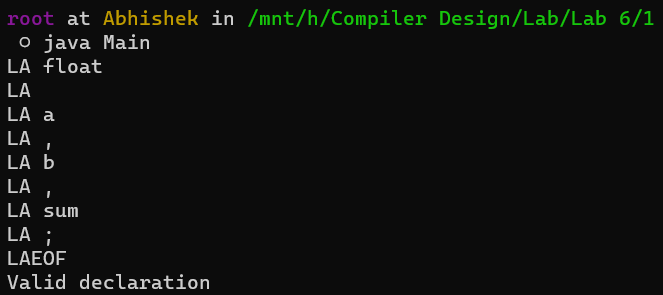


Java File

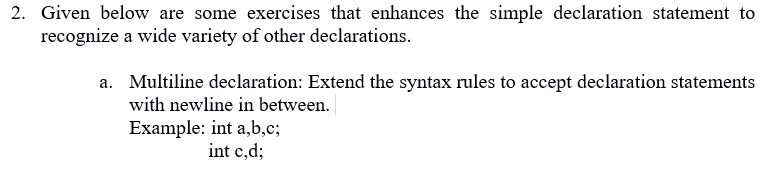


Output

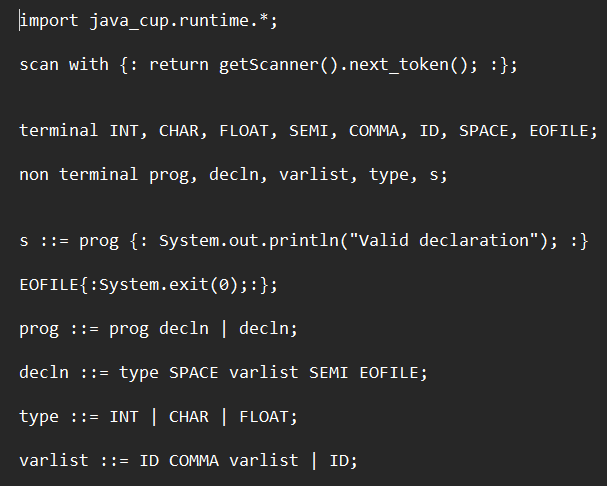




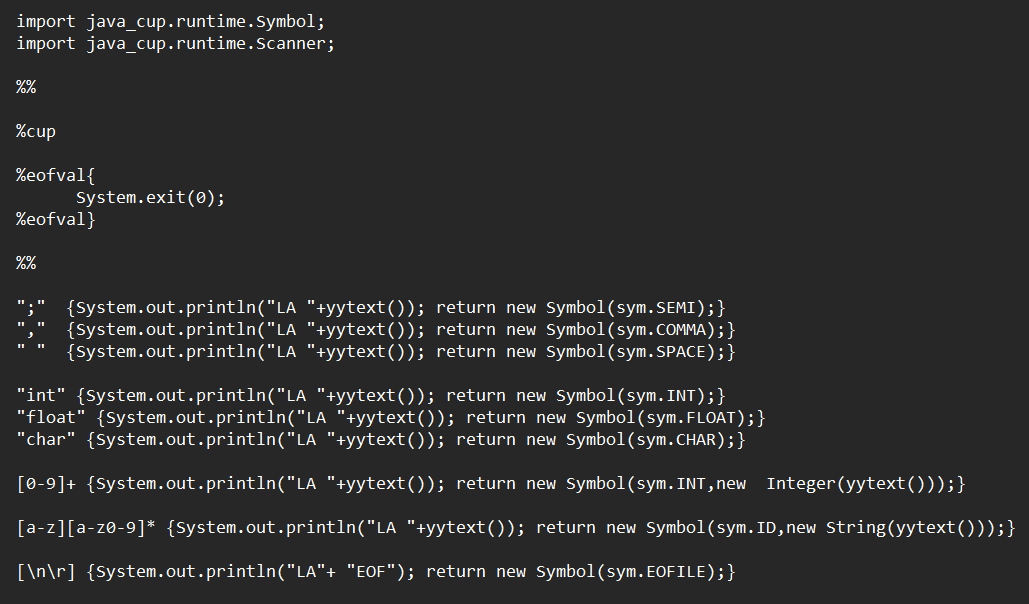




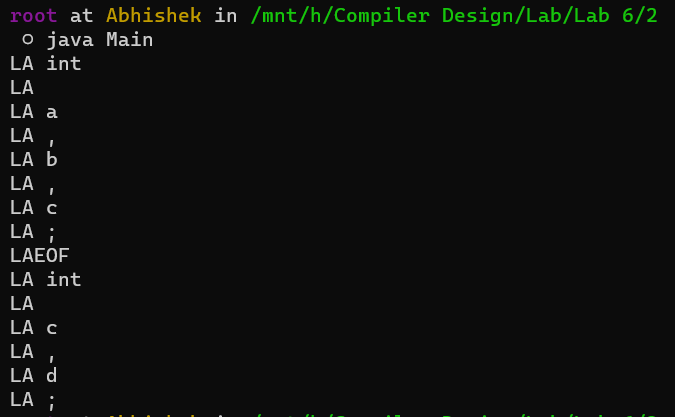
Cup File

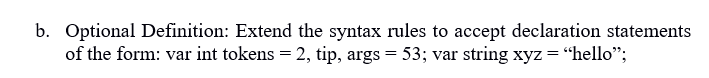


Jlex File

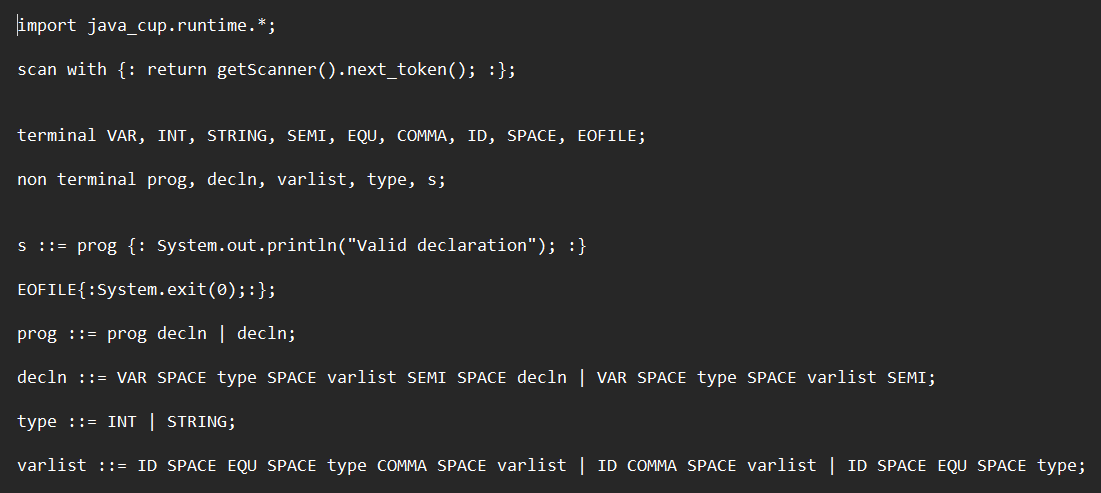


Output

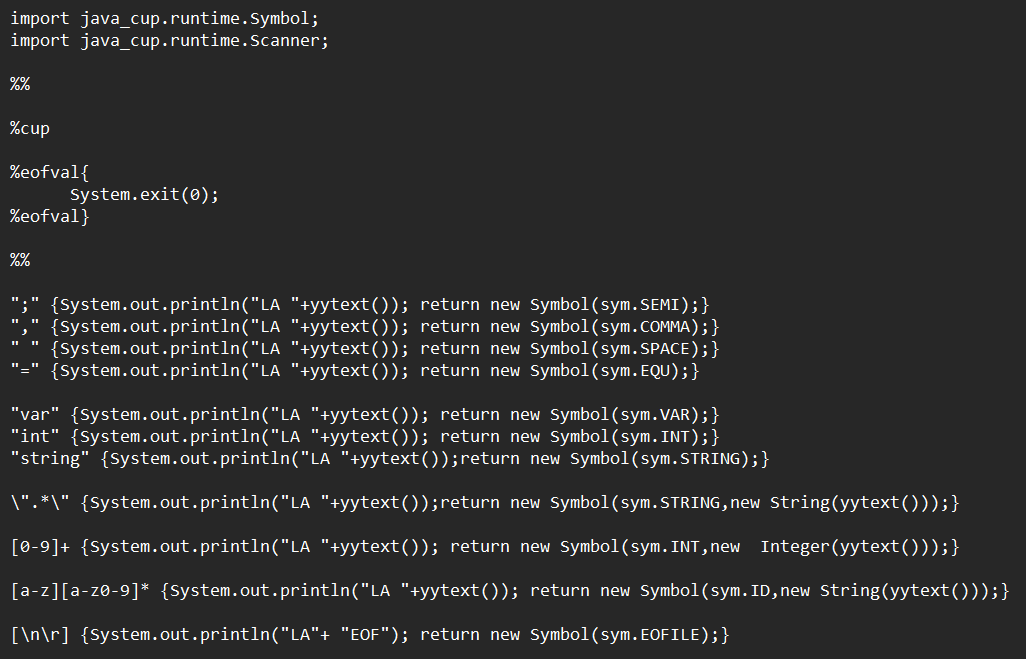




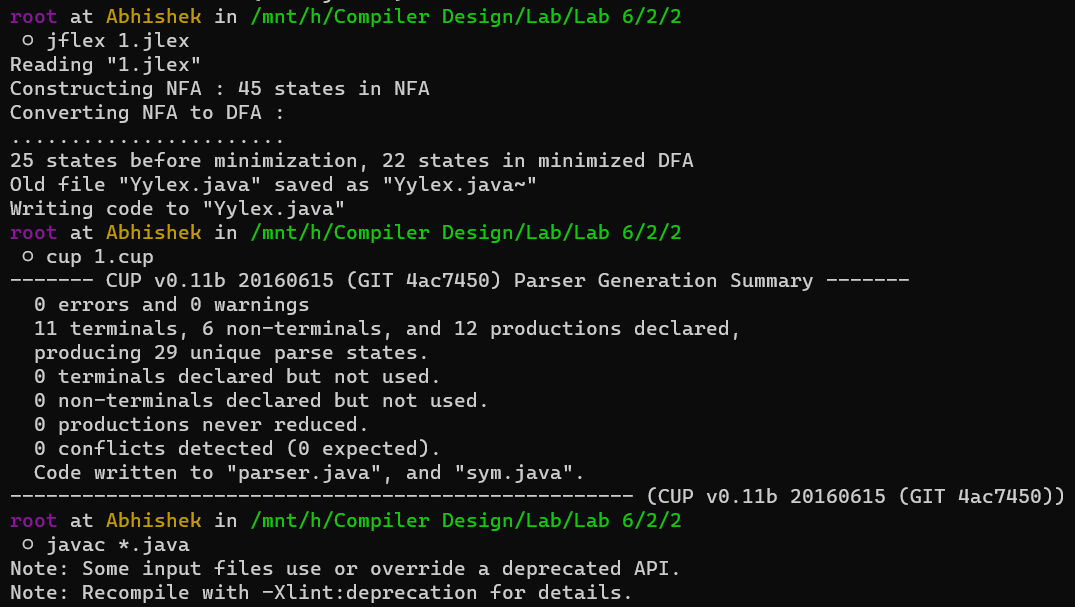
Cup File

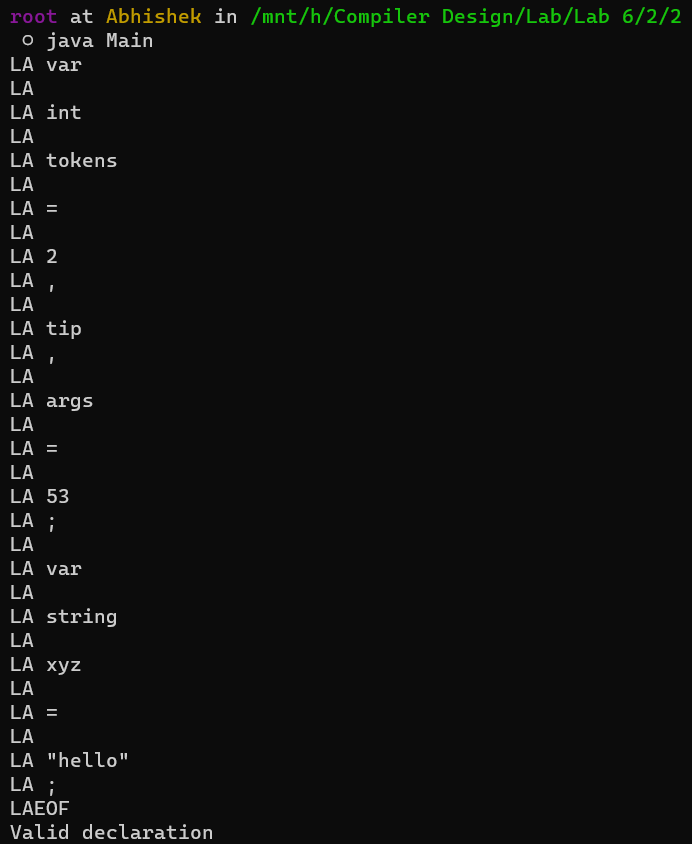


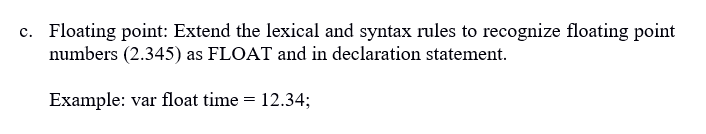
Jlex File



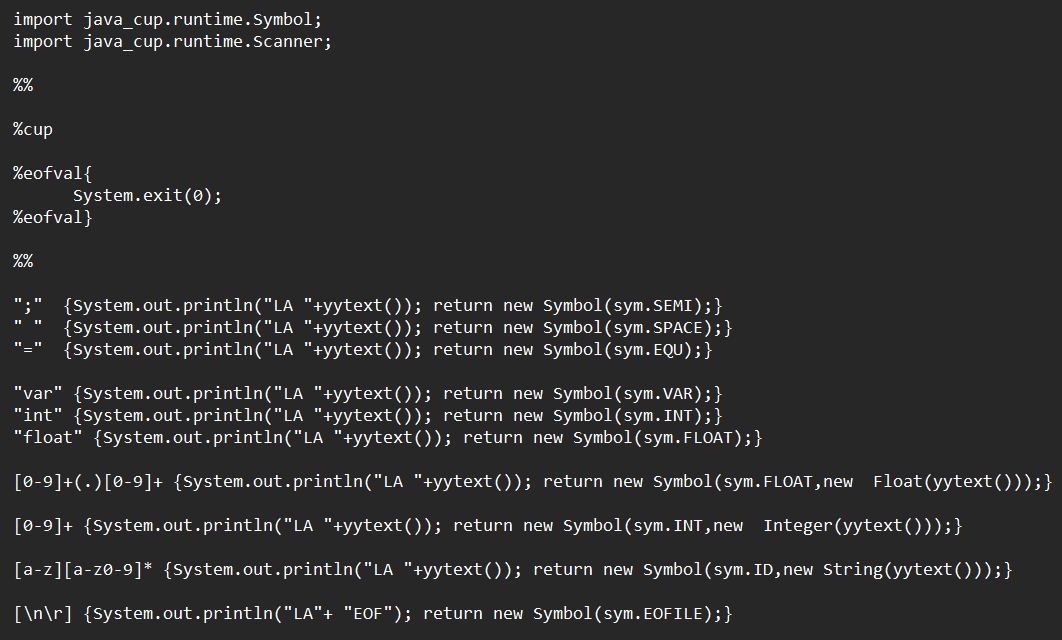
Output



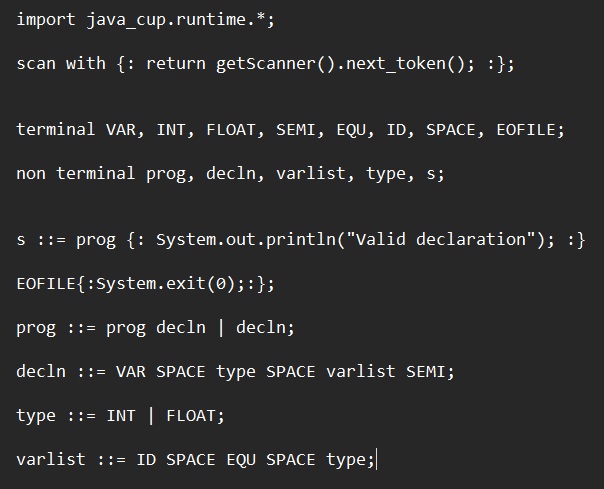




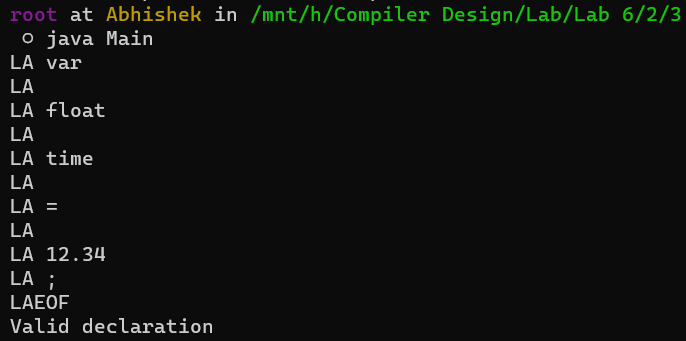
Jlex File

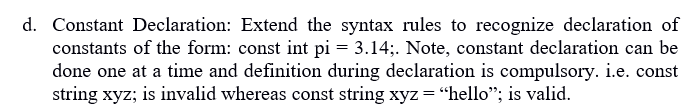


Cup File

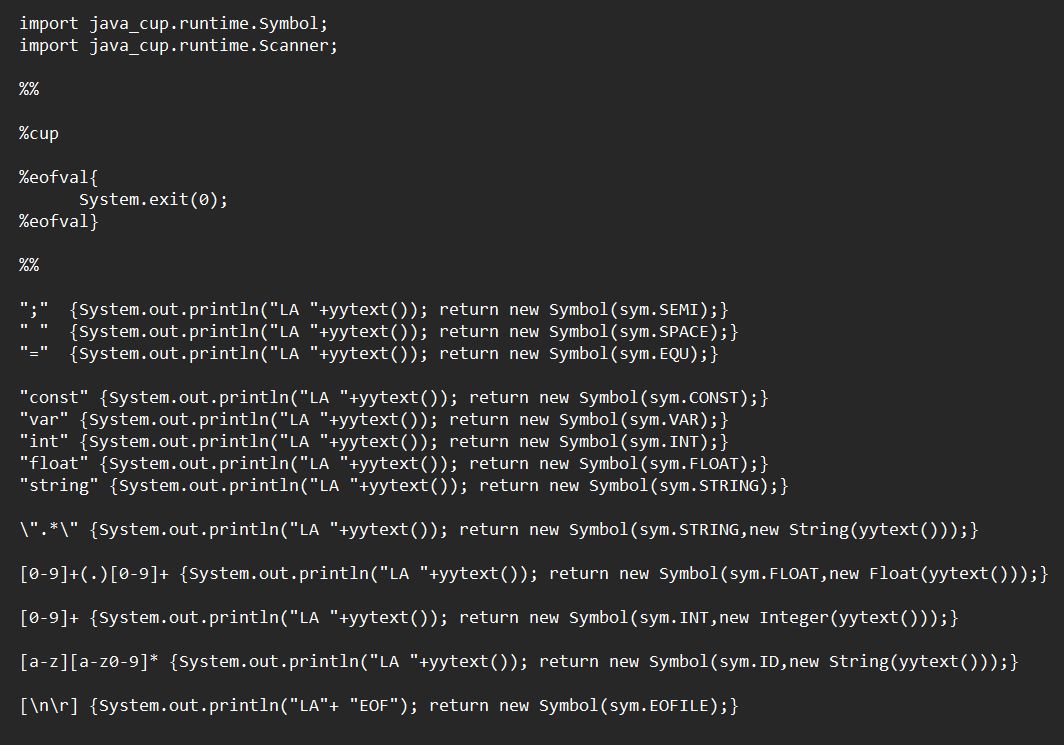


Output

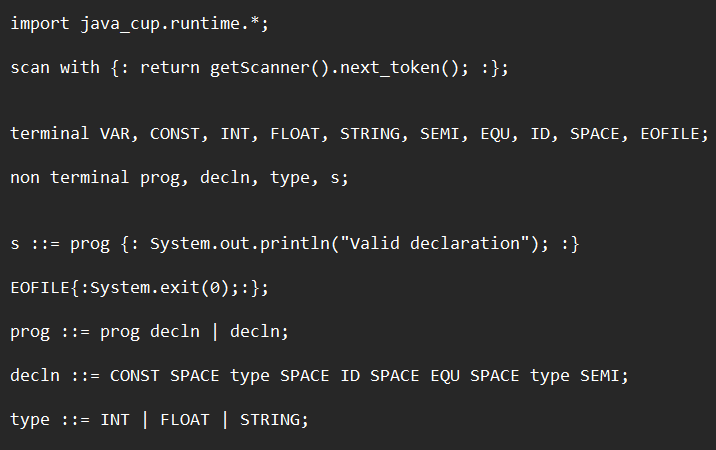




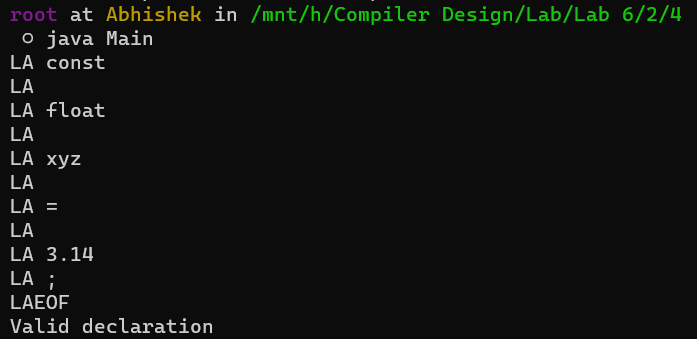
Jlex File

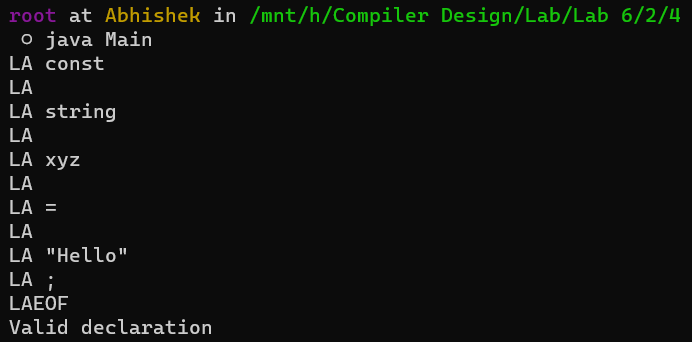


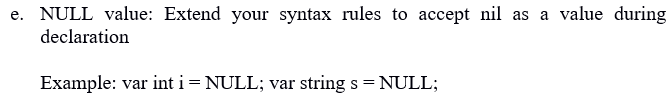
Cup File



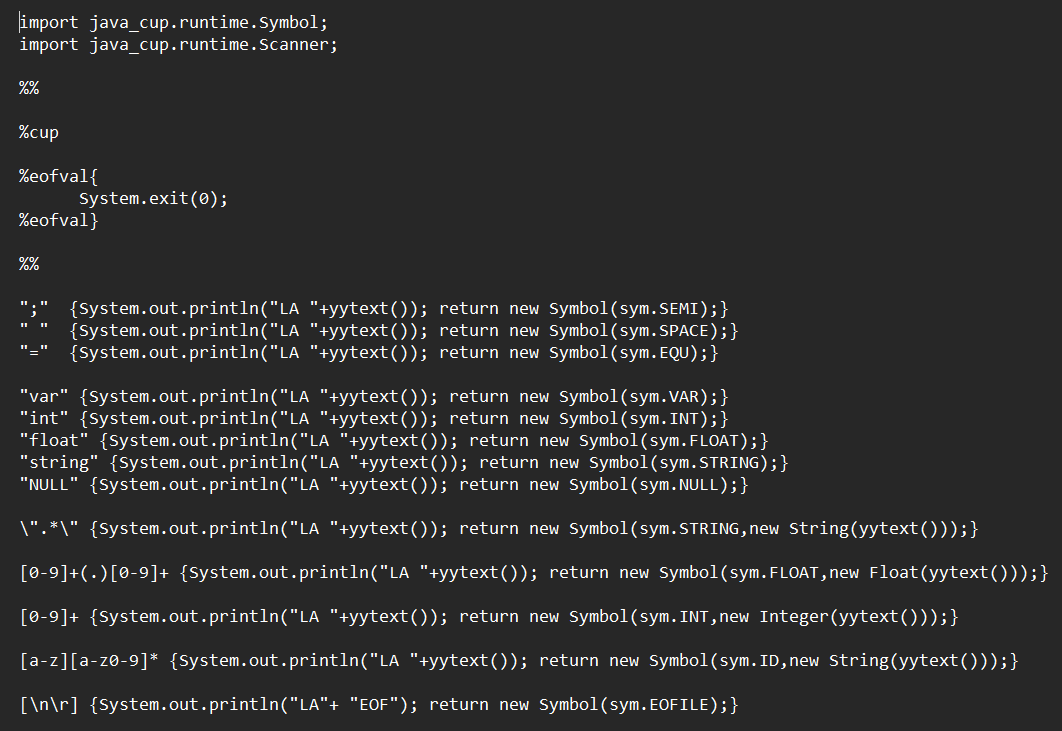
Output



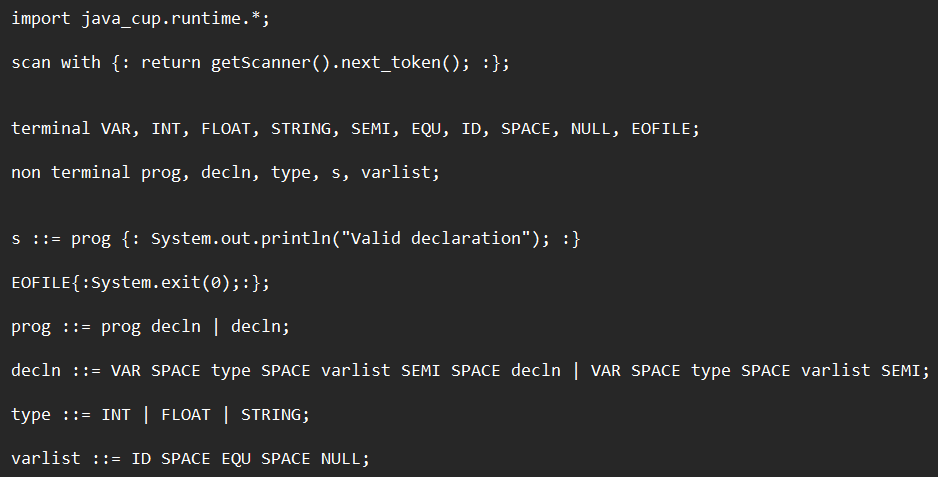




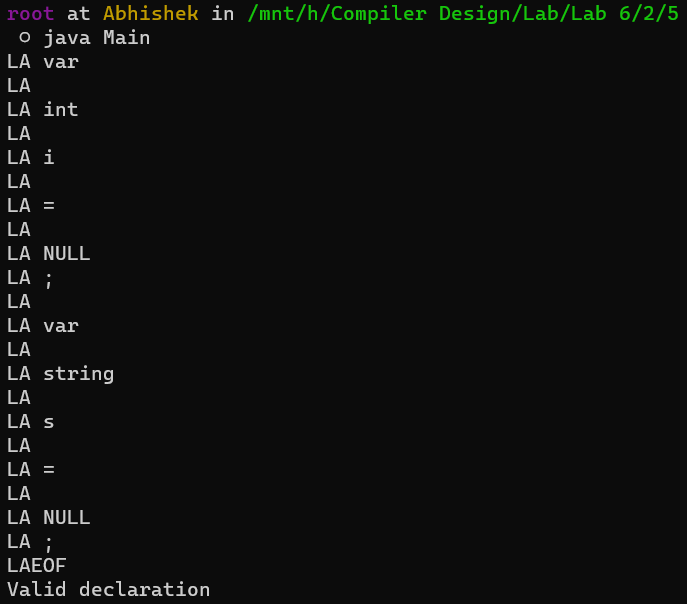
Jlex File



Cup File

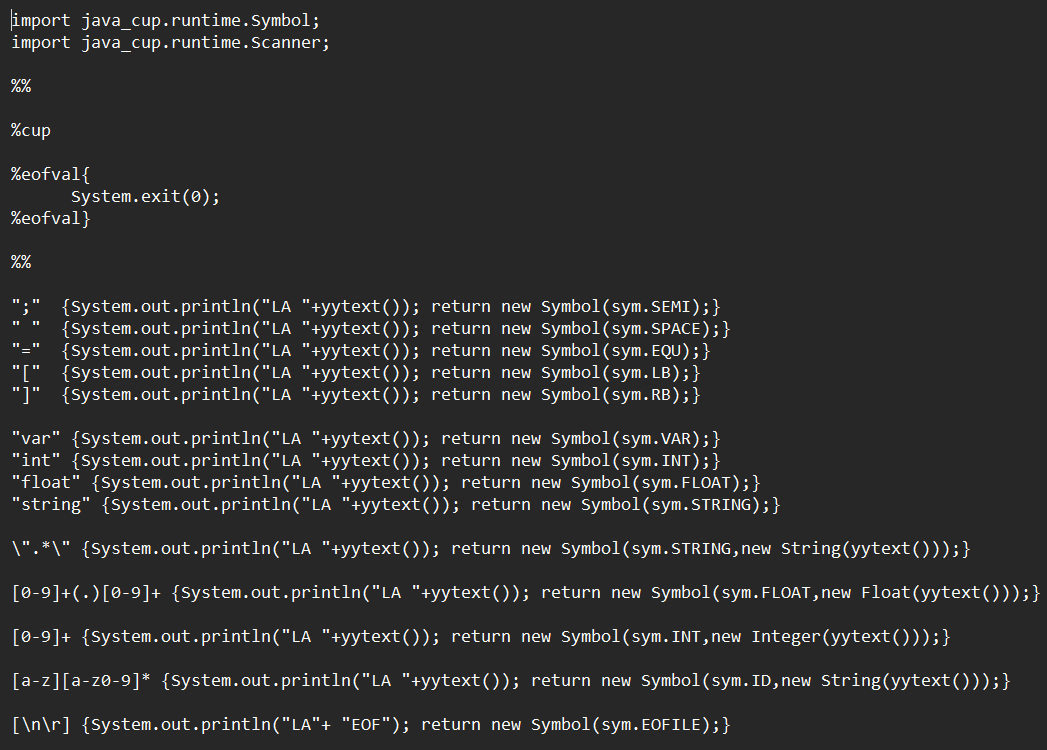


Output

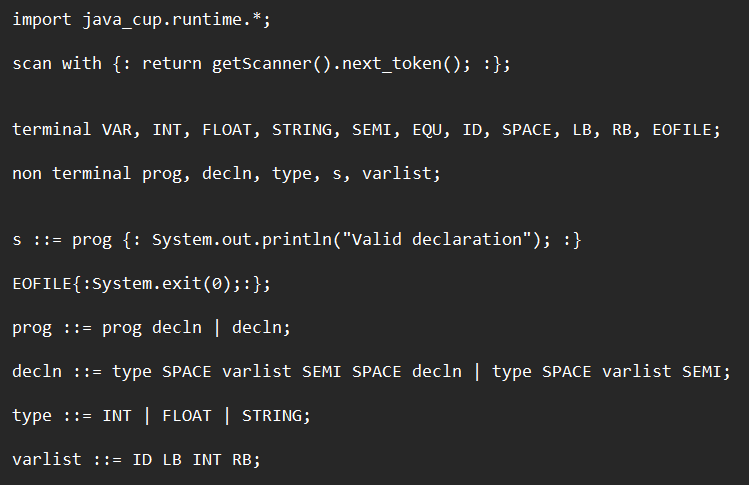




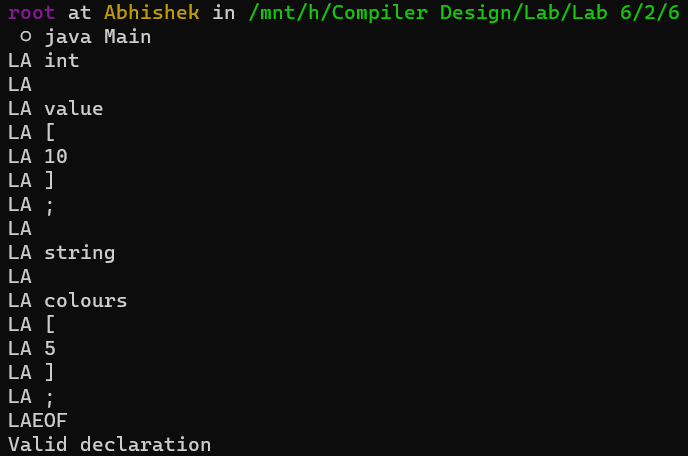
Jlex File

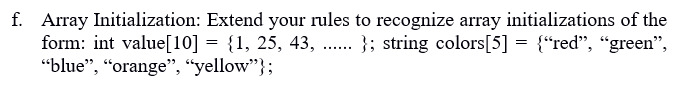


Cup File

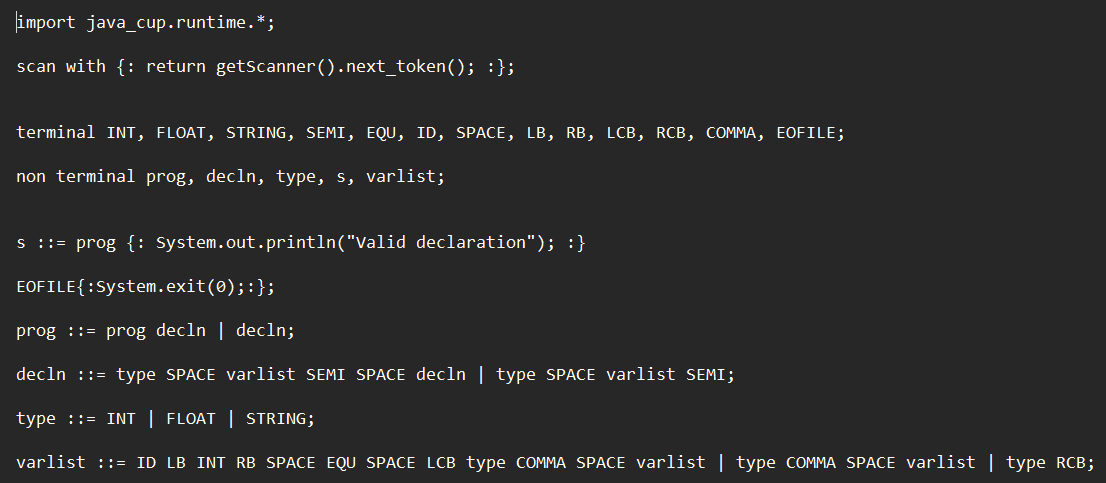


Output

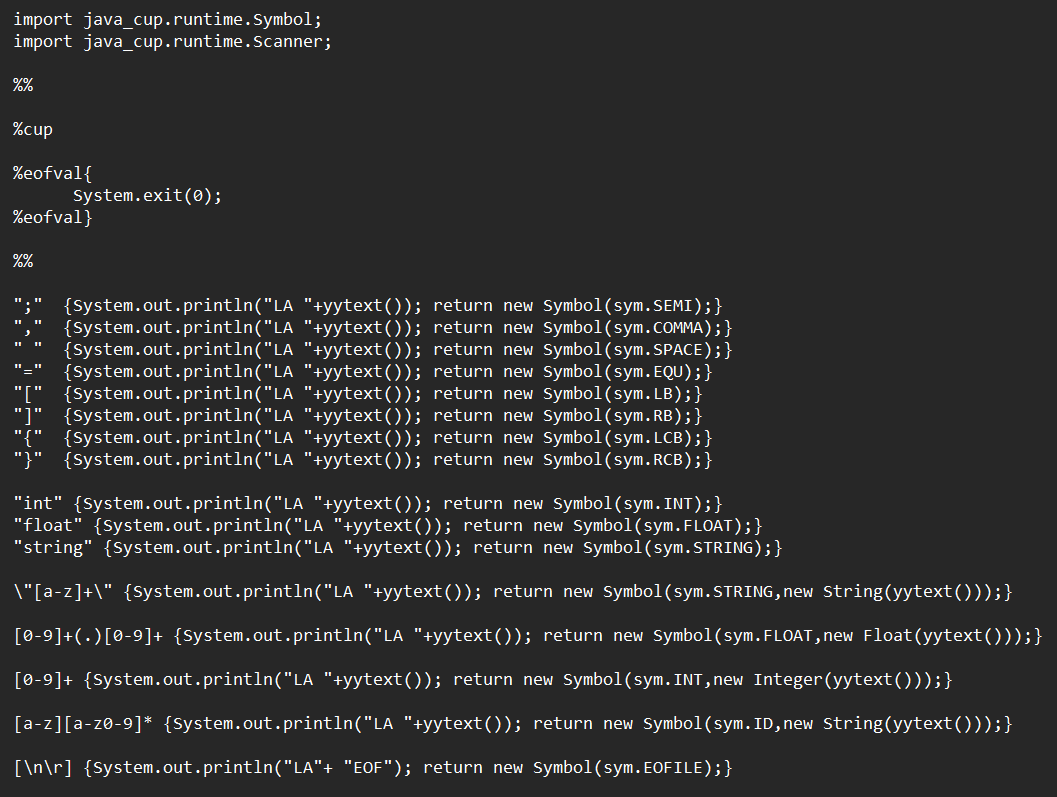




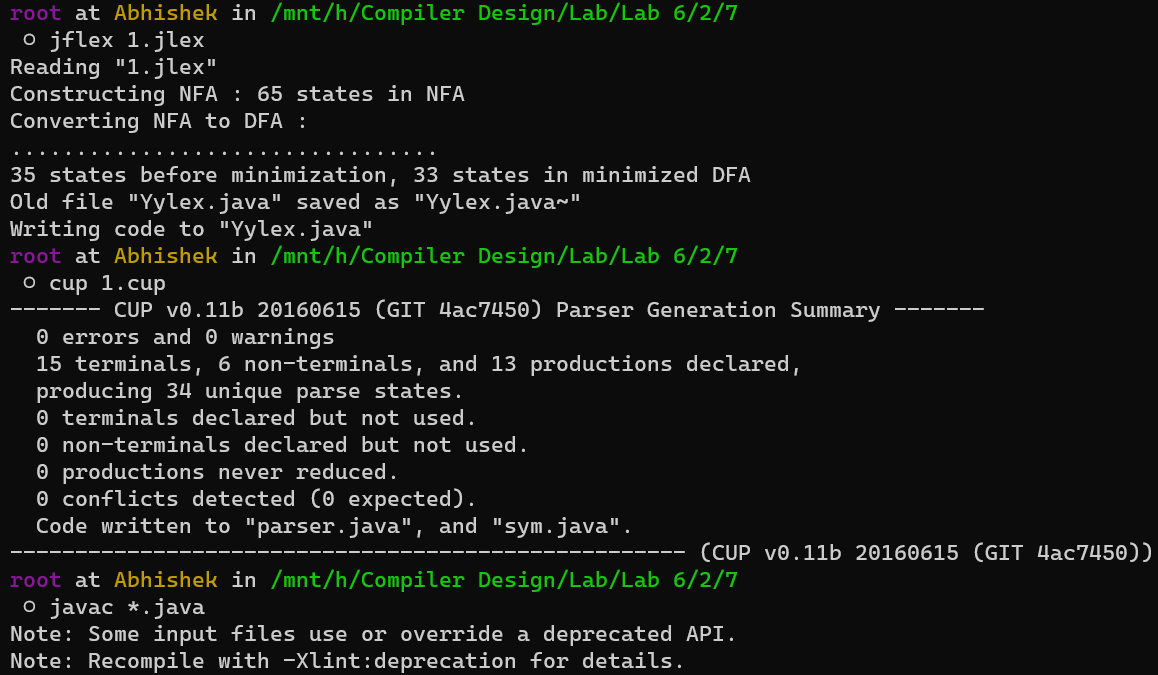
Cup File

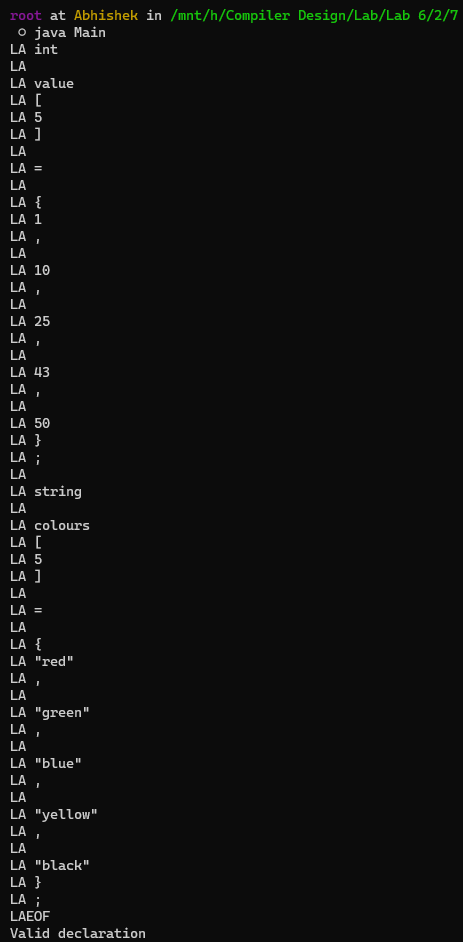


Jlex File



Output





Thankyou!!